

# 58. WORKSHOP ON MICROELECTRONICS

July 7, 2017 // Reutlingen University, Germany

## CALL FOR PAPERS



### Topics

- Design of Application Specific Integrated Circuits (ASIC)
- Microelectronics Technology
- Analog IC Circuit Design
- Digital IC Circuit Design
- FPGA/CPLD Design
- Methodology and Architecture
- System Design of IC's
- CAE for IC Design
- IC Design related Technologies
- Microelectronics Education

### Important Dates

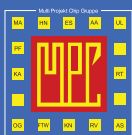
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Cooperating Organisation  
Solid-State Circuit Society Chapter  
IEEE German Section

