

63. WORKSHOP ON MICROELECTRONICS

21 February, 2020 // Hochschule Mannheim, Germany

CALL FOR PAPERS



Topics

- Embedded Systems
- Design of Application Specific Integrated Circuits (ASIC)
- Microelectronics Technology
- Analog IC Circuit Design
- Digital IC Circuit Design
- FPGA/CPLD Design
- Methodology and Architecture
- System Design of IC's
- CAE for IC Design
- IC Design related Technologies

Important Dates

Paper submission deadline	21 January, 2020
Acceptance notification	24 January, 2020
Submission of final version	21 February, 2020

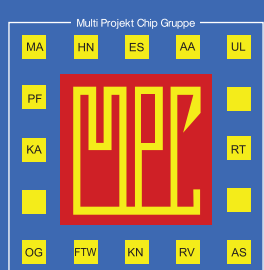
The best student presentation will be awarded during the workshop !

Steering Committee H.-P. Bürkle, J. Gerlach, R. Gessler, J. Giehl, E. Hennig, F. Kesel, W. Lindermeir, E. Mackensen, W. Rülling, C. Schick, A. Siggelkow, A. Terzis

Program Committee J. Aghassi-Hagmann, J. Giehl, E. Mackensen, A. Terzis

Local Organisation J. Giehl (Hochschule Mannheim) **Conference Place** Hochschule Mannheim

Conference Language German and English



Cooperating Organisation
Solid-State Circuit Society Chapter
IEEE German Section

