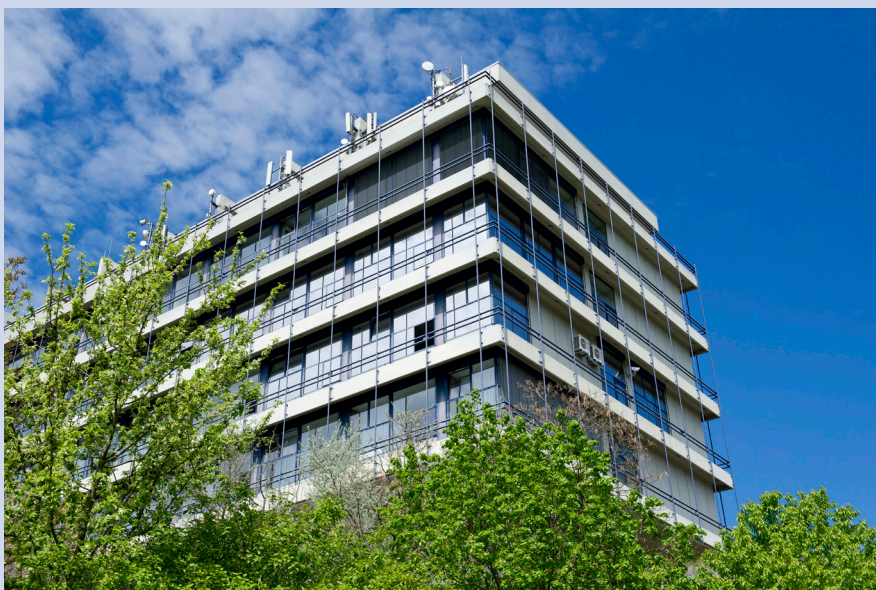


64. WORKSHOP ON MICROELECTRONICS

24 June, 2022 // Hochschule Esslingen, Germany

CALL FOR PAPERS



Topics

- Design of Application Specific Integrated Circuits (ASIC)
- Microelectronics Technology
- Analog and Digital IC Circuit Design
- Embedded Systems
- FPGA/CPLD Design
- Methodology and Architecture
- System Design of IC's
- CAE for IC Design
- IC Design related Technologies
- Microelectronics Education

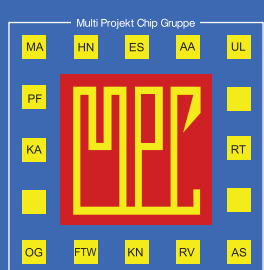
Important Dates

Paper submission deadline	16 May, 2022
Acceptance notification	20 May, 2022
Submission of final version	24 June, 2022

The best student presentation will be awarded during the workshop!

Steering Committee H.-P. Bürkle, A. Terzis, J. Gerlach, R. Gessler, J. Giehl, F. Kesel, H.-J. Ng, W. Lindermeir, A. Siggelkow, D. Benyoucef, C. Schick, E. Mackensen, E. Hennig

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Cooperating Organisation
Solid-State Circuit Society Chapter
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